

FIGURE 1

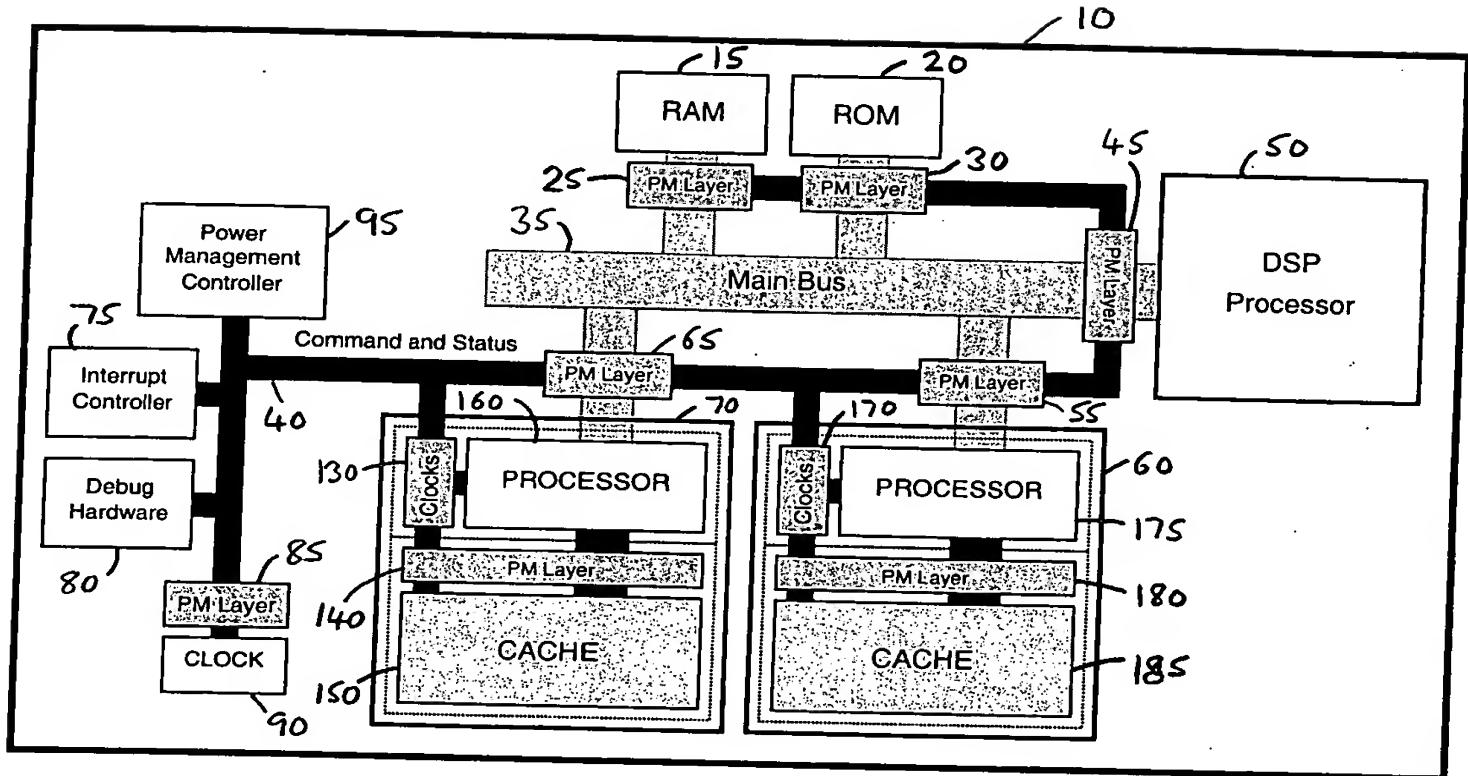


FIGURE 2

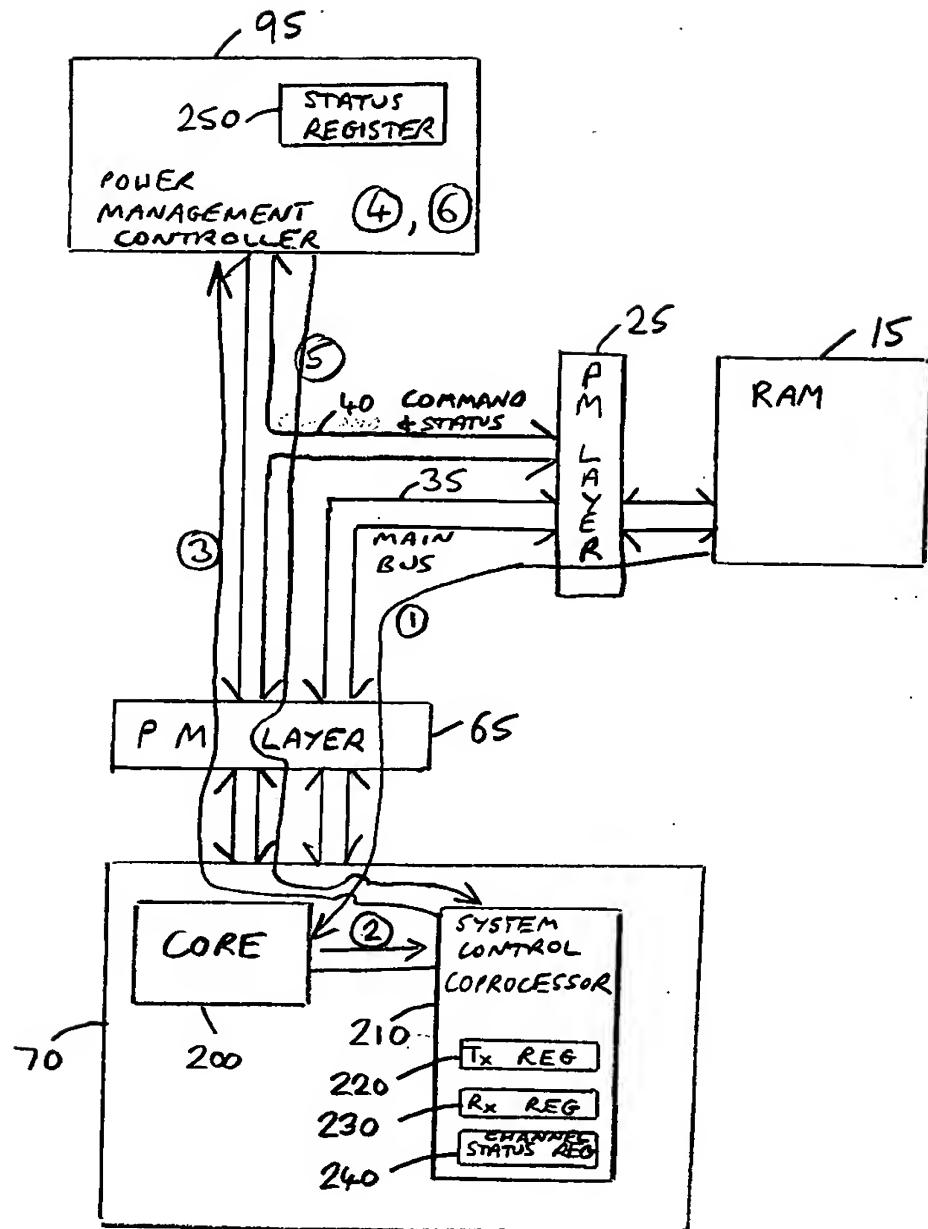


FIG. 3

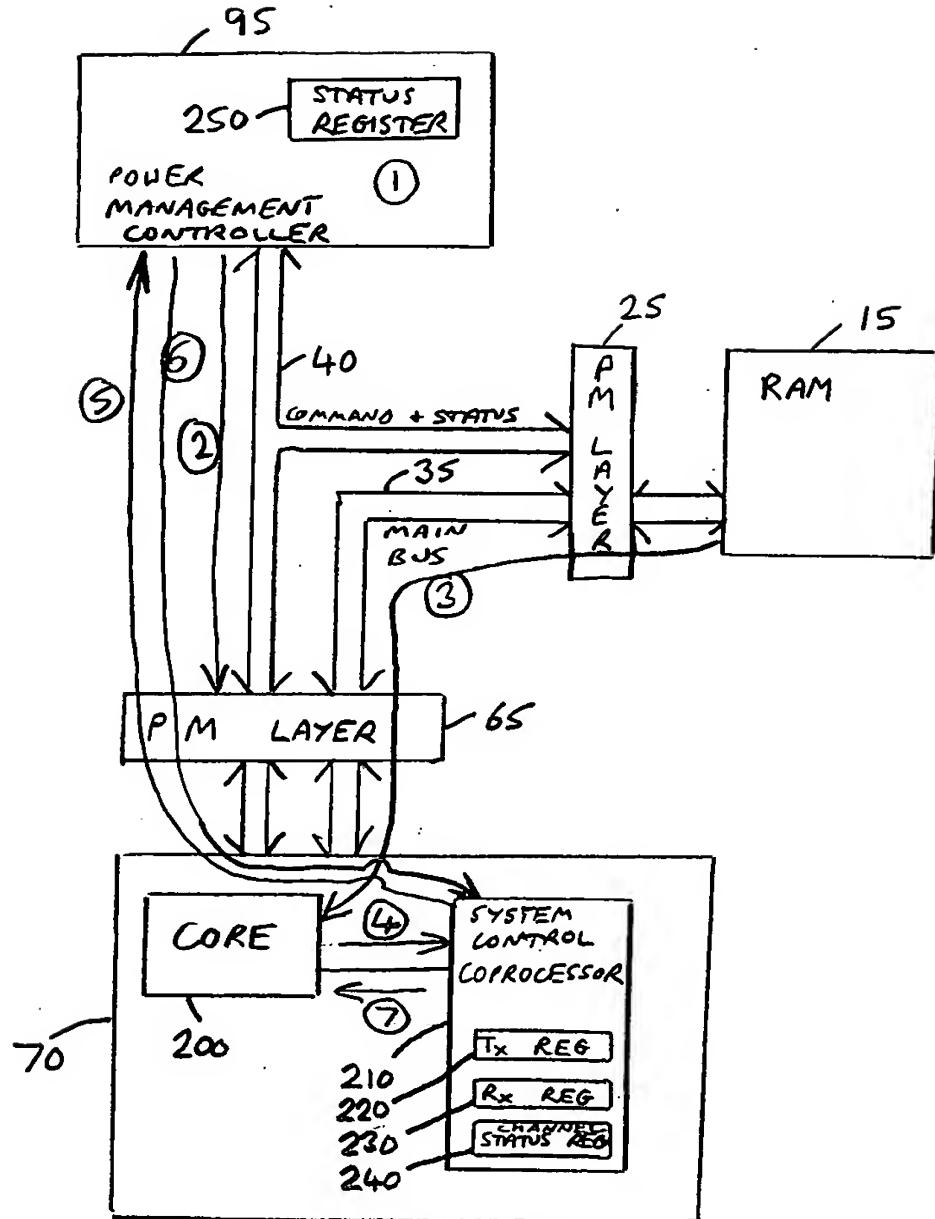


FIG. 4

240

bit	31	28	27		2	1	0
	Version		SBZ		W	R	
reset					1	1	

FIGURE 5A

230

bit	31	30		8	7	4	3	0
	E		SBZ		State		SBZ	
reset	0							

FIGURE 5B

220

bit	31	30		8	7	4	3	0
	E		SBZ		State		SBZ	
reset	0							

FIGURE 5C

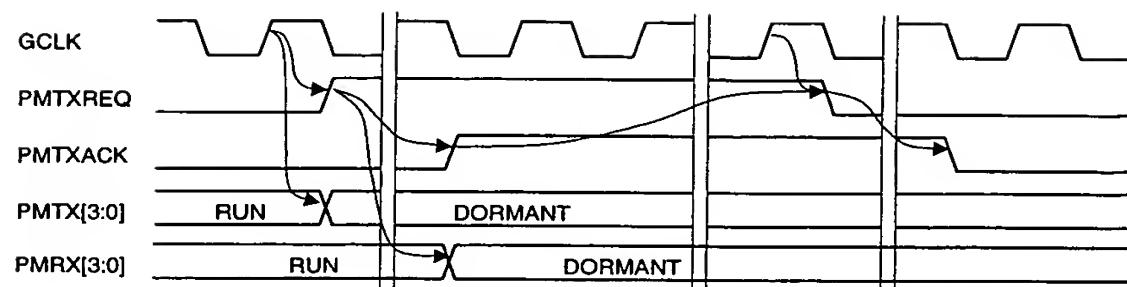


FIGURE 6A

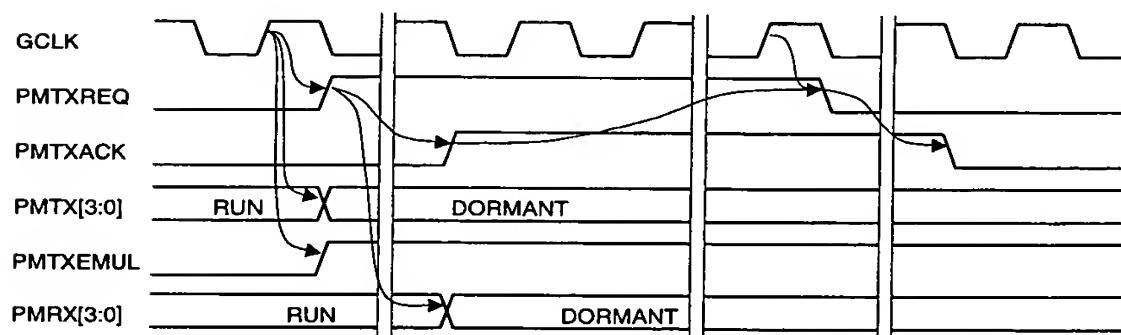


FIGURE 6B

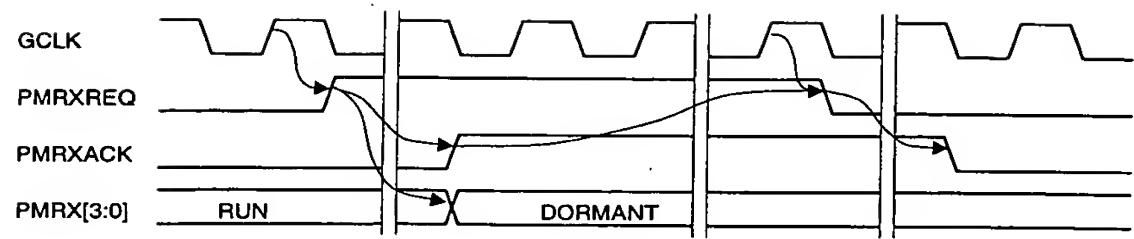


FIGURE 7A

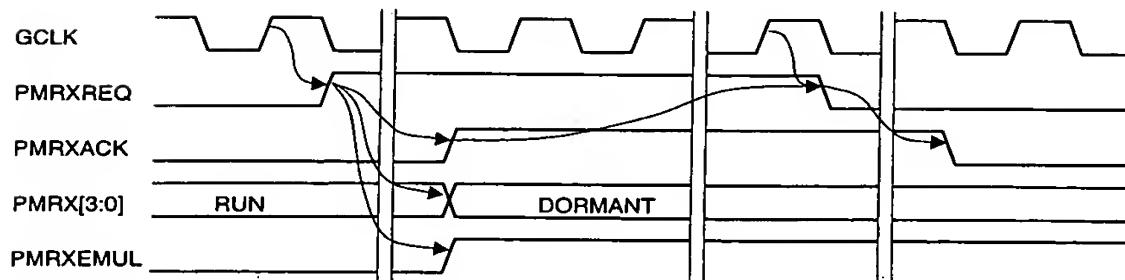
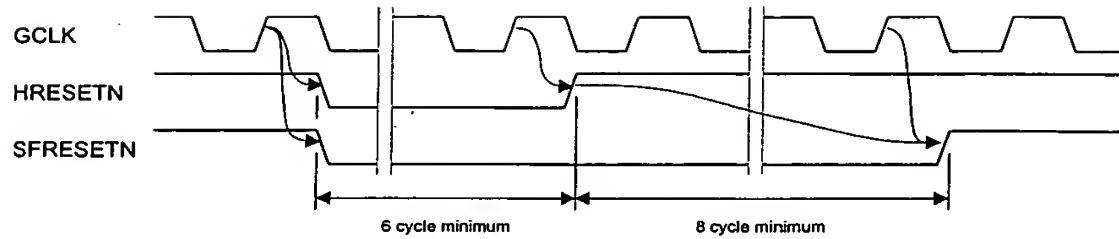
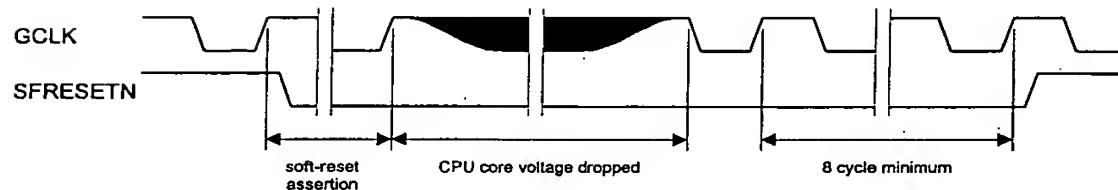


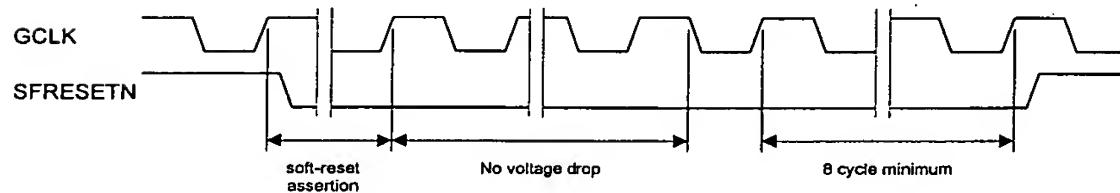
FIGURE 7B



**FIGURE 8A**



**FIGURE 8B**



**FIGURE 8C**